

1c474 U.S. PTO
10/064301
07/01/02

BEST AVAILABLE COPY

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

7c943

APP. NUM 10064301	FILING DATE 07/01/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER THA1.1.1
**APPLICANTS: Brown Jeffrey; Mann Randy;					
**CONTINUING DATA VERIFIED: None TN					
** FOREIGN APPLICATIONS VERIFIED: None TN					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no TN		ATTORNEY DOCKET NO BUR920010185	
TITLE : Method and structure for scalable, low-cost polysilicon capacitor in a planar dram U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg. Figs. Drwg. Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)